

Claims

1. Driver circuit with
 - a circuit node,
 - 5 - at least two first transistors, the load sections of which are switched in series and connect the circuit node with a voltage,
 - at least two second transistors, the load sections of which are switched in series and connect the circuit node
 - 10 with a reference potential, and
 - a control circuit, which is designed in order to regulate at least a first control voltage on at least one transistor of the at least two first transistors and at least a second control voltage on at least one transistor
 - 15 of the at least two second transistors dependent on a voltage at the circuit node.
2. Driver circuit according to claim 1, characterized in that the control circuit is also designed in order to
- 20 change the driver circuit dependent on an enabling signal into an active or an inactive state.
3. Driver circuit according to claim 2, characterized in that the control circuit is also designed in order to
- 25 regulate the control voltages in the active state in such a manner that the transistors controlled by the control voltages are approximately in saturation.
4. Driver circuit according to claim 2 or 3,
- 30 characterized in that the control circuit is also designed in order to reduce the control voltages by a pre-set voltage in the inactive state, if at least one

threshold value is exceeded by the voltage on the circuit node.

5 5. Driver circuit according to claim 4, characterized in that the control circuit comprises an electrical path from the circuit node to the reference potential, which electrical path has at least one diode to pre-set the at least one threshold value.

10 6. Driver circuit according to claim 5, characterized in that the path also has a switch, which is controlled by the enabling signal.

15 7. Driver circuit according to claim 5, characterized in that the path has a resistor.

8. Driver circuit according to claim 4, characterized in that the control circuit comprises an electrical path, which comprises the load section of a transistor
20 controlled by the enabling signal, said transistor being connected on the one hand with a further voltage and on the other hand with a diode and serves to regulate the control voltages in the inactive state, as long as the voltage on the circuit node is below the at least one
25 threshold value.

9. Driver circuit according to claim 2, characterized in that the driver circuit is configured in such a manner that in the active state it consumes no static power.

30 10. Driver circuit according to claim 1, characterized in that it is implemented in CMOS technology and the transistors are MOSFET's.

11. Driver circuit according to claim 10, characterized
in that the at least two first transistors are p-channel
MOSFET's and the at least two second transistors are n-
5 channel MOSFET's.

12. Driver circuit according to claim 10, characterized
in that a gate control is provided for controlling the
gate voltage of at least one transistor of the at least
10 two first transistors.

13. Driver circuit according to claim 10, characterized
in that a well control is provided for controlling the
well voltages of the at least two first transistors.
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